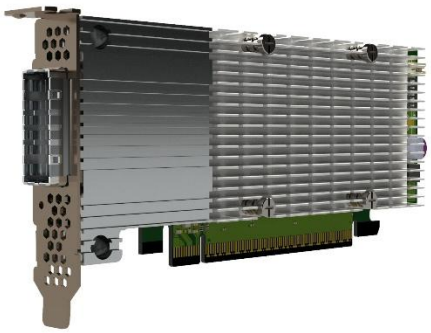
# MXH672 PCIe 6.0 x16 CDFP Transparent Adapter



## Features

- ✓ PCI Express 6.0 - 64.0 GT/s per lane
- ✓ Microchip Switchtec® PCIe 6.0 PFX
- ✓ Link compliant with PCIe 1.0, 2.0, 3.0, 4.0, 5.0 and 6.0
- ✓ Host Clock isolation support,
  Automatic support for a host running CFC or SSC
- ✓ x16 CDPF connectors
- ✓ Low Profile, Half Length PCIe form factor
- ✓ Support for both copper and fiber cables
- ✓ FLASH for multiple system configurations
- ✓ Link status LED through the face plate
- ✓ ~100ns port-to-port latency
- ✓ eXpressWare™ Board Management software license

The card is delivered with a passive heat sink by default, but it is also available with a fan for use on an open desktop.

The MXH672 PCIe 6.0 Transparent Host/Target Adapter is our High-Performance IO product. It is designed to the PCI-SIG Card Electromechanical (CEM) Specification 5.0 and the PCI-SIG CopprLink 1.0 specification and employs the Microchip Switchtec® PCIe 6.0 PFX switch to facilitate reliable and fast host-to-IO communication.

Application developers using the MXH672 host/target adapter can harness its impressive PCIe 6.0 performance while benefiting from application-to-application latency under 500 nanoseconds. Additionally, it is fully compatible with our eXpressWare software suite.

www.dolphinics.com/products/MXH672.html

## Configurations

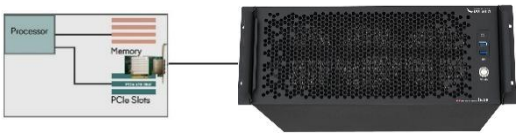
The MXH672 enables the establishment of the following PCIe network topologies:

**Single-Node Transparent IO:** The card enables a transparent IO connection to a device compliant to the PCI-SIG CopprLink 1.0 specification – e.g. Dolphins' range of PCIe 5.0 eBox 4 and eBox 4 Pro Expansion products.

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**Multiple-Node Network:** Larger IO networks can be realised using the Dolphins MXS610 switch. Please contact Dolphin for details.

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Topology Examples

# Specifications

| | | | |
|---|---|---|---|
| PCI Express | ➢ Base Specification 6.0<br>➢ CopprLink Specification 1.0<br>➢ Card Electromechanical Specification 5.0 | Operating Environment | ➢ Operating Temperature: 0˚C - 45˚C (32˚F - 113˚F)<br>➢ Airflow: TBD<br>➢ Relative Humidity: 5% -95% non-condensing |
| Port-to-Port latency | ~100ns | Storage Environment | ➢ Storage Temperature: -40˚C to 70˚C (-40˚F to 158˚F)<br>➢ Relative Humidity 95% (non-condensing) at 35°C |
| Active Components | Microchip Switchtec® PCIe 6.0 PFX Switch | Mechanical Dimensions | Low profile, half-length, 167.65mm (6.6 inches) x 68.90 mm (2.731 inches) |
| | | Software | Optional Dolphin Board Management software |
| Max Link Speeds | 1024 Gb/s | Usage Modes | Transparent Host/Target |
| Configuration | DIP-switch | Operating Systems | Windows, Linux |
| Topologies | Host to IO | Pending Regulatory markings | ➢ CE<br>➢ FCC Class B<br>➢ KCC<br>➢ Canada/IC<br>➢ RCM |
| | | Regulatory Compliant | ➢ Reach<br>➢ RoHS<br>➢ UL94V-0 |
| Cable Connections | ➢ CDFP connector for copper/fiber cables<br>➢ Supports x16 cables. | Mounting Brackets | ➢ Full height Bracket installed.<br>➢ Half-height bracket is included in the shipping box. |
| Maximum power rating | ➢ TBD | Product Codes | ➢ MXH672 Host/Target Transparent Adapter – Passive Heatsink<br>➢ MXH672F Host/Target Transparent Adapter – Active fan Heatsink |
| Typical power rating | ➢ TBD | Pending Regulatory Approvals | ➢ EN 55032:2012<br>➢ EN 55035:2017<br>➢ EN 61000-3-2:2014<br>➢ EN 61000-3-3:2013<br>➢ EN 61010-1:2010<br>➢ EN 61326-1:2013<br>➢ ICES-003<br>➢ KS C 9832:2019, KS C 9835:2019<br>➢ 47 CFR Part 15, Subpart B (Clause 15.107 and 15.109) in conjunction with ANSI C63.4:2014<br>➢ CISPR 35:2016 Edition 1.0 (CISPR/I/412/CDV) Korean Harmonized standard, KN 35 |
| Port Power | ➢ TBD | | |